

	Type	Hits	Search Text	DBs	Time Stamp
1	IS&R	1	("5241536").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:23
2	BRS	1	wo-200013093-\$ did.	US-PGPUB; USPAT; EPO; JPO; DERWENT	2004/04/02 09:30
3	IS&R	1	("5634006").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:30
4	IS&R	1	("5668951").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:31
5	IS&R	1	("5231631").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:31
6	IS&R	1	("5572678").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:32
7	IS&R	1	("5377327").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:32
8	IS&R	1	("5668951").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:36
9	IS&R	1	("5745486").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/02 09:48
10	BRS	354	burst adj switch\$3	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
11	BRS	13	(burst adj switch\$3) and master and slave and controller	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
12	BRS	5	((burst adj switch\$3) and master and slave and controller) and @ad<"20001229"	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
13	BRS	518	burst near switch\$3	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
14	BRS	19	(burst near switch\$3) and master and slave and controller	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
15	BRS	11	((burst near switch\$3) and master and slave and controller) and @ad<"20001229"	US-PGPUB; USPAT; EPO; JPO	2004/04/02 09:52
16	BRS	1120	data adj network and ((main core principle major) near (node unit module device switch))	US-PGPUB; USPAT; EPO; JPO	2004/04/02 12:56

	Type	Hits	Search Text	DBs	Time Stamp
17	BRS	28	(data adj network and ((main core principle major) near (node unit module device switch))) and (master near controller)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 12:57
18	BRS	13	((data adj network and ((main core principle major) near (node unit module device switch))) and (master near controller)) and burst	US-PGPUB; USPAT; EPO; JPO	2004/04/02 12:57
19	BRS	7	(((data adj network and ((main core principle major) near (node unit module device switch))) and (master near controller)) and burst) and @ad<"20001229"	US-PGPUB; USPAT; EPO; JPO	2004/04/02 12:59
20	BRS	4	(((data adj network and ((main core principle major) near (node unit module device switch))) and (master near controller)) and burst) and @ad<"20001229") and (TDMA schedul\$4)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 13:01
21	BRS	26	space near switch\$4 and schedul\$4 and (TDMA (time near slot) timeslot\$1)and (burst (high near (capacity traffic))) and controller and @ad<"20001229" and (data near network)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:49
22	BRS	3029	370/230-240.ccls.	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:49
23	BRS	42	370/230-240.ccls. and (space near switch\$4)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:50
24	BRS	2418	370/230-240.ccls. and switch\$4	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:50
25	BRS	2323	(370/230-240.ccls. and switch\$4) and data	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:50
26	BRS	1808	((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229"	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:50
27	BRS	816	(((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:51
28	BRS	0	(((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller) and scheduller	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:51
29	BRS	267	(((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller) and schedul\$4	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:52

30	BRS	16	(((((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller) and schedul\$4) and master and slave	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:53
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	Type	Hits	Search Text	DBs	Time Stamp
31	BRS	20	(((((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller) and schedul\$4) and slave and (master core)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 15:57
32	BRS	19	(((((370/230-240.ccls. and switch\$4) and data) and @ad<"20001229") and controller) and schedul\$4) and slave and (master core)) and (burst\$1 capacity traffic congestion)	US-PGPUB; USPAT; EPO; JPO	2004/04/02 16:17
33	IS&R	1	("5241536").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/03 14:51
34	BRS	1	burst adj scheduler and @ad<"20001229"	US-PGPUB; USPAT; EPO; JPO	2004/04/03 14:52
35	IS&R	1	("6405257").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/03 16:28
36	IS&R	1	("5107489").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/03 17:07
37	IS&R	1	("6118762").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/05 08:13
38	IS&R	1	("0536212").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/05 08:14
39	IS&R	1	("5636212").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/04/05 08:14
40	BRS	7539	370/380,230,235,236,357,358,360,381,389,230.1.ccls.	US-PGPUB; USPAT; EPO; JPO	2005/01/28 18:05
41	BRS	5744	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and S40	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:00
42	BRS	7539	370/380,230,235,236,357,358,360,381,389,230.1.ccls.	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:01
43	BRS	82	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and S42 and (burst with request)	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:35
44	BRS	7	S43 and (space near switch\$3)	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:02
45	BRS	201	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst near4 request\$1) and (space with (switch\$3 core controller master slave))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:38

	Type	Hits	Search Text	DBs	Time Stamp
46	BRS	125	S45 and (port\$1 same input\$4 same output\$4)	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:38
47	BRS	41	S47 and (connect\$5 with (setup (set adj2 up) establish\$4))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 19:46
48	BRS	16	S49 and (connect\$5 with (setup (set adj2 up) establish\$4))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:07
49	BRS	14	S50 and ((master main principle primary first) near4 (controller cpu processor))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:09
50	BRS	9	S52 and ((slave second\$5 another other) near4 (controller cpu processor))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:12
51	BRS	72	S46 and schedul\$5	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:26
52	BRS	123	S46 and (schedul\$5 tdm\$1 pipeline ((in an) near order))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:37
53	BRS	19	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst near request\$1) and (space near (switch\$3 core controller master slave))	US-PGPUB; USPAT; EPO; JPO	2005/01/30 20:41
54	BRS	1	"20020085491"	US-PGPUB; USPAT; EPO; JPO	2005/01/31 11:32
55	BRS	201	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst near4 request\$1) and (space with (switch\$3 core controller master slave))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 13:50
56	BRS	125	S56 and (port\$1 same input\$4 same output\$4)	US-PGPUB; USPAT; EPO; JPO	2005/01/31 13:36
57	BRS	72	S57 and schedul\$5	US-PGPUB; USPAT; EPO; JPO	2005/01/31 13:36
58	BRS	16	S58 and (connect\$5 with (setup (set adj2 up) establish\$4))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 13:36
59	BRS	5	S59 and ((master main principle) near4 (controller cpu processor))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 13:36
60	BRS	70	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst with (request\$1 signaling setup connection)) and (space near (switch\$3 "core node" "core unit" "core device" controller "master controller" "central controller" "main controller" "slave controller" "slave processor"))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 14:03

	Type	Hits	Search Text	DBs	Time Stamp
61	BRS	7	S61 and scheduler	US-PGPUB; USPAT; EPO; JPO	2005/01/31 14:05
62	BRS	53	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst with (request\$1 signaling)) and (space near (switch\$3 "core node" "core unit" "core device" controller "master controller" "central controller" "main controller" "slave controller" "slave processor"))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:16
63	BRS	10	S63 and schedul\$4	US-PGPUB; USPAT; EPO; JPO	2005/01/31 14:14
64	BRS	2807	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst with (request\$1 signaling))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:25
65	BRS	2195	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst near4 (request\$1 signaling connection))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:17
66	BRS	1	S66 and ((bufferless "non-buffer" (no adj2 (buffer queue))) near2 space near2 switch\$3)	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:27
67	BRS	1	S65 and ((bufferless "non-buffer" (no adj2 (buffer queue))) near2 space near2 switch\$3)	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:25
68	BRS	69	(@ad<"20001229" @rlad<"20001229" @rlan<"20001229") and (burst with (request\$1 signaling)) and (tim\$3 adj2 lock\$3)	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:31
69	BRS	4	S70 and (space near2 switch\$3)	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:31
70	BRS	1	S65 and ((bufferless "non-buffer" (no adj2 (buffer queue))) with (space near2 switch\$3))	US-PGPUB; USPAT; EPO; JPO	2005/01/31 17:33